

# CV Summary

Personal information	
Name:	Remigiusz Wiśniewski
E-mail address:	<a href="mailto:R.Wisniewski@issi.uz.zgora.pl">R.Wisniewski@issi.uz.zgora.pl</a>
Personal website:	<a href="http://staff.uz.zgora.pl/rwisniew/en/index.html">http://staff.uz.zgora.pl/rwisniew/en/index.html</a>
Appointments	
2019 – present	<b>Professor</b> at University of Zielona Gora, Poland
2021 – present	<b>Head of Division</b> of Information Systems and Cybersecurity (Institute of Control & Computation Engineering)
April/May 2024	Visiting Professor at New Jersey University of Technology, USA (internship)
April 2019	Visiting Professor at University of California, Berkeley, USA (internship)
2018 – 2020	Head of Division of Applied Informatics (Institute of Electrical Engineering)
2009 – 2018	Assistant Professor, University of Zielona Gora
2003 – 2009	Assistant, University of Zielona Gora
2001 – 2002	Senior Application Engineer, Alatec Inc./Aldec Inc., Gdansk, Poland
2001 (3 months)	Senior Application Engineer, Alatec Inc./Aldec Inc., Las Vegas, NV, USA
2000 (3 months)	Application Engineer, Aldec Inc., Las Vegas, NV, USA
1999 – 2000	Application Engineer, Aldec Inc., Zielona Gora, Poland
Education	
2018	D.Sc. (habilitation), Silesian University of Technology, Poland
2008	Ph.D. (with distinction), University of Zielona Gora, Poland
2003	M.Sc., University of Zielona Gora, Poland
Research interests	
<ul style="list-style-type: none"><li>• Concurrent control systems, embedded systems, CPS, manufacturing systems</li><li>• Modeling, analysis, verification and simulation of concurrent systems specified by Petri nets</li><li>• Prototyping of systems implemented in programmable devices (PLD, CPLD, FPGA)</li><li>• Dynamic and static partial reconfiguration of systems implemented in an FPGA</li><li>• Prototyping of logic circuits in hardware description languages (Verilog, VHDL)</li><li>• Cryptology (development, cryptanalysis of algorithms, prime numbers in cryptology)</li><li>• Applied cryptology (software and hardware implementation of algorithms)</li></ul>	
Memberships in scientific associations and committees	
2022 – present	Committee on Computer Science and Automatic of Polish Academy of Science (Poznan Branch)
2019 – present	IEEE IES Technical Committee on Industrial Informatics
2019 – present	Advisory Board at Polish Ministry of Science and Higher Education ("Industrial Doctorate" program)
2019 – present	Senior Member of the IEEE
2017 – present	Member of the IEEE
2017 – present	IEEE Industrial Electronics Society (IES)
2017 – present	IEEE Systems, Man, and Cybernetics Society (SMC)
2019 – 2020	Polish Committee for Standardization (KT 182)
2010 – 2017	Board of the Branch of the Polish Information Processing Society
2009 – 2020	Polish Society for Theoretical and Applied Electrical Engineering
Publications	
More than 150 publications, including: 2 books (monographs), 10 book chapters and 60+ peer-reviewed journal papers (including IEEE Transactions). The complete list of publications is available at: <a href="https://pers.uz.zgora.pl/publikacje/pracownik-2156?from=1960&amp;to=2050">https://pers.uz.zgora.pl/publikacje/pracownik-2156?from=1960&amp;to=2050</a>	